

ABSTRACT

An in-plane switching mode liquid crystal display device comprises a substrate, a pixel region, a common bus line, a thin film transistor, a data electrode, a passivation layer over the data electrode and the thin film transistor, and a common electrode. The pixel region lies on the substrate. The common bus line is aligned in the pixel region. The thin film transistor is coupled to the pixel region and the pixel regions comprises a gate electrode and a gate insulator having a portion overlying the gate electrode. The data electrode lies over the gate insulator and has a portion overlying the common bus line to form a first storage capacitor. The passivation layer overlies the data electrode and the thin film transistor. The common electrode overlies the passivation layer and has a portion overlying the data electrode to form a second storage capacitor.